IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Craig BARRACK et al.

Application filed herewith

Filed: March 29, 2004

For: Compact Packet Switching Node Storage Architecture Employing Double Data Rate

Synchronous Dynamic RAM

INFORMATION DISCLOSURE STATEMENT

(SUBMISSION AFTER FILING OF AN APPLICATION BUT BEFORE FINAL REJECTION OR NOTICE OF ALLOWANCE)

Mail Stop Patent Application Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §§ 1.97 and 1.98, applicant(s) hereby submit(s) an Information Disclosure Statement for consideration by the Examiner.

I. LIST OF PATENTS, PUBLICATIONS OR OTHER INFORMATION

The patents, publications or other information submitted for consideration by the Office are listed on PTO-1449, attached hereto.

II. COPIES

X Submitted herewith is a legible copy of (i) each U.S. and foreign patent; (ii) each publication or that portion which caused it to be listed; and (iii) all other information or that portion which caused it to be listed.

III. CONCISE EXPLANATION OF THE RELEVANCE (check at least one box)

- a. X Except as may be indicated below in (b), all of the patents, publications or other information are in the English language or were cited in an English language Search Report (concise explanation not required).
- b. A concise explanation of the relevance of all patents, publications or other information listed that is not in the English language is as follows:
- c. __ The following additional information is provided for the Examiner's consideration.

1 V .		THIS IDS IS BEING FILED UNDER 37 CFR § 1.97(0). (check one box)
	a. <u>X</u>	within three months of the filing date of a national application (37 CFR § 1.97(b)(1)). No fee or certification is required.
	b	within three months of the date of entry of the national stage as set forth in § 1.491 in an international application (37 CFR § 1.97(b)(2)). No fee or certification is required.
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	The un	ndersigned hereby states that
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	Respectfully submitted,
March	Lawrence E. Laubscher, Jr. Registration No. 28,233 1160 Spa Road, Suite 2B Annapolis, Maryland 21403 Telephone: (410) 280-6608
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b. _ no item of information contained in the IDS was cited in a communication from a

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INFO	RMATION DI	SCI	OSURE	Filing Date	March 29, 2004
STATEMENT BY APPLICANT		First Named Inventor	Craig BARRACK		
	(use as many sheets as	necess	ary)	Group Art Unit	
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Sheet	1	of	1	Attorney Docket No.	23471

	U.S. PATENT DOCUMENTS							
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MICRON TECHNOLOGY, INC.; "Double Data Rate (DDR) SDRAM" Produ Specification; 2003; pages 1-83; Micron Technology, Inc.; Boi Idaho, USA.								

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